IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Amit Ramchandran

Patent No.: 7,194,605 B2

Issued:

March 20, 2007

Filed:

July 24, 2003

For:

CACHE FOR

INSTRUCTION SET ARCHITECTURE USING INDEXES TO ACHIEVE

COMPRESSION

Certificate of Correction Branch Commissioner for Patents P.O. Box 1450

Alexandria, VA 22313-1450

Serial No.: 10/628,083

Group Art Unit: 2183

Examiner: Eric Coleman

CERTIFICATE OF MAILING 37 CFR 1.8

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Date

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REQUEST FOR CERTIFICATE OF CORRECTION

Dear Sir:

Attached is a Certificate of Correction for correcting errors made in the claims of the printed patent.

Applicant submits that the errors mentioned above were not made by the applicant, but were made during the printing of the patent.

Respectfully submitted,

John C. Carev Registration No. 51,530

PATTERSON & SHERIDAN, L.L.P. 3040 Post Oak Blvd. Suite 1500

Houston, TX 77056

Telephone: (713) 623-4844 Facsimile: (713) 623-4846

Attorney for Applicant

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CERTIFICATE OF CORRECTION

PATENT NO: 7,194,605 B2 Page 1 of 1

APPLICATION NO.: 10/628,083

DATED: March 20, 2007

INVENTOR(S): Amit Ramchandran

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In column 11, line 53, please replace "In", with --in--

In column 12, line 2, please replace "In", with --in--

In column 12, line 11, please replace "Instruction", with --instruction--

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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